

Amendments to the Specification:

Please **replace** the first full paragraph on page 6, which begins with “Fig. 1 is a block diagram,” with the following amended paragraph:

Fig. 1 is a block diagram illustrating an example architecture of a set-top receiver 100. In this example, satellite dish 110 receives a signal from the service provider. The signal is transmitted to tuner module 120 containing broadcast in-band tuner 121 and out-of-band tuner 122. Broadcast in-band tuner 121 isolates a channel from a multiplex of channels and converts the channel to baseband. Out-of-band tuner 122 is used to facilitate transfer of data between the service provider and the set-top receiver. Out-of-band tuner 122 is also used to provide consumers with interactive services. Tuner module 120 also contains return path 123, allowing the user to send data to the service provider. Modulator 112 converts the incoming signal to digital and delivers the modulated signal to return path 123. From tuner 120, the signal is conveyed in MPEG (or other format) to demodulator 130. Demodulator 130 converts the analog signal to a digital bit stream containing at least video and audio signals. Demodulator 130 transmits the signal to transport demultiplexer 140. Transport demultiplexer 140 conveys the signal to audio decoder 145, video decoder 150 and data decoder 160. Audio decoder 145 conveys the audio signal to speaker 147. Video decoder 150 transmits the signal to graphics processor 160 that sends the signal to monitor 170. Data decoder 160 sends the signal to a system bus, in this example 32 bit system bus 172. System bus 172 is electronically coupled to hard drive 174 and incorporates data ports, including RS-232 port 176, USB port 178, modem port 180 and television/video out port 182. System bus 172 is also coupled to SRAM 184, DRAM 186, FLASH memory 190 and EEPROM memory 192. Finally, system bus 180 is electronically coupled to CPU 188.